

### Claims

1. A chip card with at least one application for which an implementation (4) and an entry (5) referring to the implementation (4) are present on the chip card (1), characterized in that a plurality of entries (5) referring to the same implementation (4) are present on the chip card (1).
2. The chip card according to claim 1, characterized in that the entries (5) referring to the same implementation (4) characterize different virtual applications.
3. The chip card according to either of the previous claims, characterized in that the entries (5) each contain a freely selectable information sequence (9).
4. The chip card according to claim 3, characterized in that the freely selectable information sequences (9) of those entries (5) referring to the same implementation (4) each have a different content.
5. The chip card according to either of claims 3 and 4, characterized in that the freely selectable information sequences (9) have specifications for execution of the associated implementation (4).
6. The chip card according to any of the previous claims, characterized in that it is intended for use in a handset of a mobile phone system.
7. The chip card according to claim 6, characterized in that a single implementation (4) is present for a plurality of virtual applications for proving a network access authorization.
8. The chip card according to claim 7, characterized in that an entry (5) is present for each virtual application for proving a network access authorization, the entries (5) referring to the same implementation (4) and a different network access authorization being available through each entry (5).
9. The chip card according to claim 8, characterized in that the entries (5) have different parameters that are evaluated when invoking the virtual applications for

proving a network access authorization and effectuate the use of the data belonging to the particular network access authorization.

10. A method for executing an application available on a chip card (1), characterized in that one of a plurality of entries (5) present on the chip card (1) and referring jointly to an implementation (4) of the application on the chip card (1) is evaluated, and said implementation (4) is executed in a way specified by the evaluated entry (5).
11. The method according to claim 10, characterized in that the implementation (4) present on the chip card (1) is executed in different ways depending on which of the entries (5) referring to the implementation (4) is evaluated.